No. 449 And State No.		PATENT NUMBER
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Class UE CLAS		
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	U.S. UTILITY Patent Applic	alion
	RW O.I.P.E.	PATENT DATE
	SCANNED DO Q.A. A.G.	

APPLICATION NO. 09/575551	CONT/PRIOR D	CLASS 052	SUBCLASS	3635 3635	EXAMINER Keenan
Raymon Tho Robert Ber Gary Curti Stephen Cu	ner S				
Semiconduc loading st	tor proce ation	essing sy	/stem with	wafer con	tainer docking and

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ISSUING CLASSIFICATION						
ORIGINA	AL .	CROSS REFERENCE(S)				
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets Drwg.	- Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent		l		NOTICE OF ALI	OWANCE MAILED	
subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)			
The term of this patent shall not extend beyond the expiration date		-			· - · - · · · · · · · · · · · · · · · ·	
of U.S Patent. No.				ISSUE FEE		
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